

Application No. «DirectField1» (Docket: «DirectField2»)
37 CFR 1.111 Amendment dated 09/02/2005
Reply to Office Action of 04/20/2005

AMENDMENTS TO THE ABSTRACT

Please delete the section entitled "ABSTRACT OF THE DISCLOSURE" in its entirety and substitute the following section therefor:

ABSTRACT OF THE DISCLOSURE

An apparatus in a microprocessor for executing programmed native instructions that are provided directly to the microprocessor via an external instruction bus. The apparatus includes instruction translation logic and bypass logic. The instruction translation logic retrieves macro instructions ~~provided via the external instruction bus~~, and decodes each of the macro instructions into associated native instructions ~~for execution by the microprocessor, wherein, where~~ the instruction translation logic decodes a native bypass macro instruction into an unconditional jump native instruction directing that program control be transferred to a memory address containing the programmed native instructions, and ~~wherein where~~ the memory address is ~~explicitly~~ prescribed by contents of an architectural register, the contents and the architectural register being prescribed by a macro instruction. The bypass logic is coupled to the instruction translation logic. The bypass logic disables the instruction translation logic upon detection of the native bypass macro instruction, and provides the programmed native instructions directly to execution logic for execution ~~for execution by the microprocessor~~, thereby bypassing the instruction translation logic.